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FIG. 1(a)

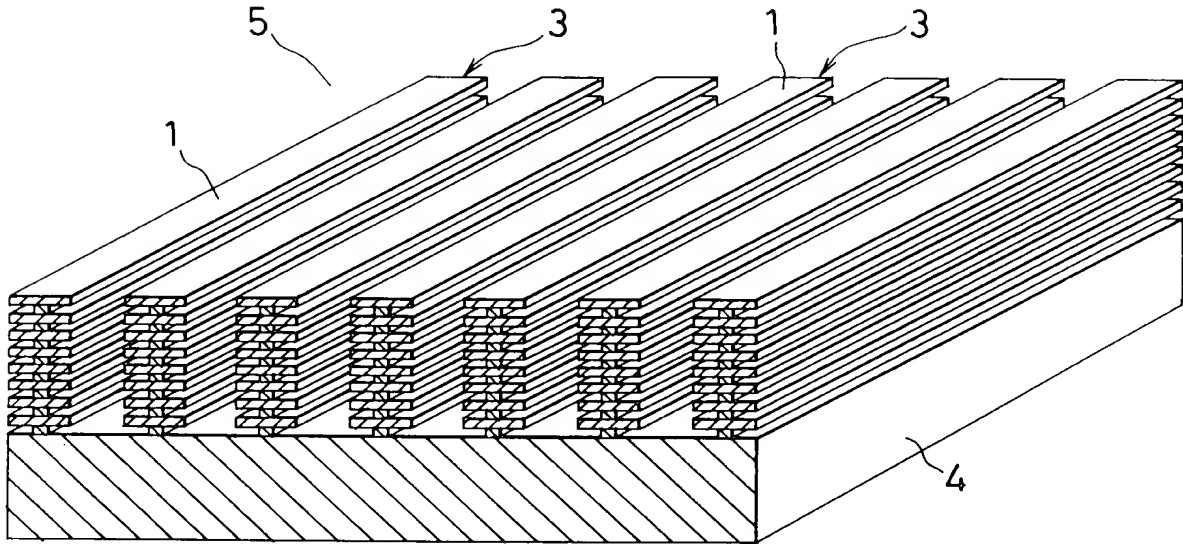


FIG. 1(b)

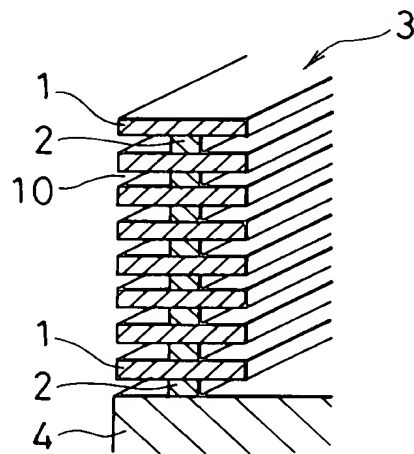


FIG. 2(b)

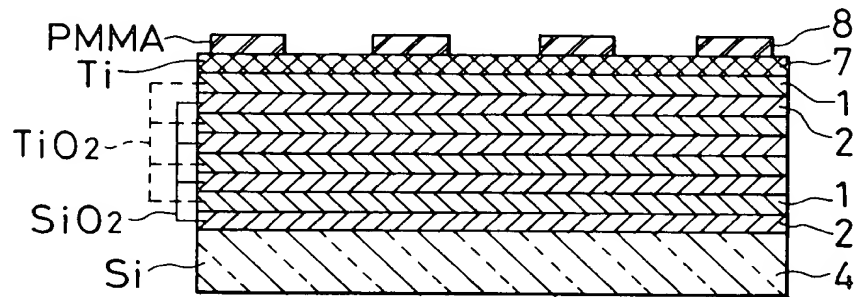
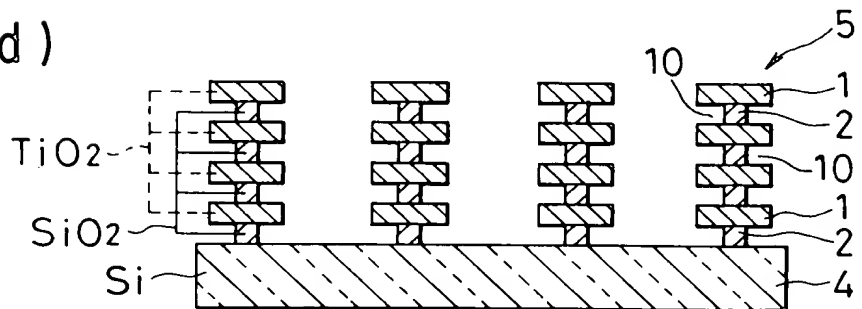


FIG. 2(d)



A perspective view of a substrate 4 with multiple stacks of semiconductor elements. The stacks are labeled 1, 2, 3, 5, and 10. The elements are labeled 10.

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FIG. 5

